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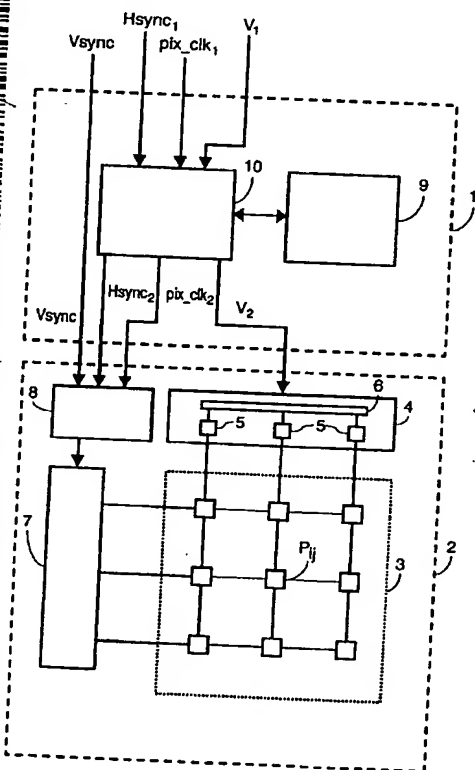
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(54) Title: CIRCUIT FOR DRIVING A DISPLAY PANEL



(57) Abstract: A circuit for driving a display panel (3) that comprises a matrix of pixels (P_{ij}), which matrix comprises the plurality of rows (i) and columns (j), which circuit comprises; a) an input for receiving an input signal (V_1) comprising pixel values (s_{ij}) for the plurality of rows (i) in a frame to be displayed by at least some of the pixels (P_{ij}), each pixel value (s_{ij}) determining a light output of a pixel (P_{ij}), b) a memory (9) for storing the received pixel values (s_{ij}), c) processing circuitry (10) for analysing the pixel values (s_{ij}) in each of the plurality of rows (i) and for generating a row timing signal ($Hsync_2$) for addressing a subset of the plurality of rows (i) for substantially a duration of a row time ($trow_2(i)$), and d) a video output for supplying an output signal (V_2) comprising output pixel values to pixels (P_{ij}) in the subset of rows (i) being addressed. The processing circuitry (10) is arranged to determine each row time ($trow_2(i)$) in dependence on at least one pixel value (s_{ij}) from among the pixel values (s_{ij}) for the subset of rows (i) being addressed during that row time ($trow_2(i)$).

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